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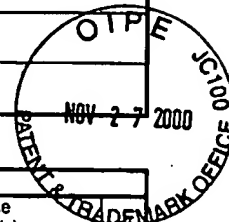
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				Application Number	09/638,025
				Filing Date	August 14, 2000
				First Named Inventor	Ashok N. Rudrapatna
				Group Art Unit	3661
				Examiner Name	Not Yet Assigned
Sheet	2	of	2	Attorney Docket Number	RUDRAPATNA-8



OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS		
Examiner Initials	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc), date, page(s), volume-issue number(s), publisher, city and/or country where published.
TP		"Implementation of an Adaptive Antenna Array Using the TMS320C541," KIM PHILLIPS et al, Digital Signal Processing Solutions, Application Report SPRA532, Texas Instruments, April 1999, pp. 2-11 and title page
TP		"Parallel Processing of Adaptive Beamforming Algorithms,," SHONDA L. WALKER, Capstone Project Proposal, EEL 6935 - Parallel Computer Architecture, 3 pp.

TP  
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